

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,898,698 B1
APPLICATION NO. : 09/533042
DATED : May 24, 2005
INVENTOR(S) : Ryuichi Sunayama et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 18, line 29, insert --a-- after "only".

Col. 18, line 54, change "recognize" to --recognized--.

Col. 19, line 51, change "fore" to --for a--.

Col. 20, line 24, change "specking" to --specifying--.

Col. 21, line 17, change "cell" to --call--.

Col. 22, line 2, insert --is-- after "return".

Col. 22, line 5, change "Information" to --information--.

Col. 22, line 52, change ":" to --;--.

Col. 24, line 5, delete "comparing" after "circuit" and insert --make a comparison between--.

Col. 24, line 9, change "said" to --the--; and after "a" insert --result of the--.

Col. 24, line 10, delete "result".

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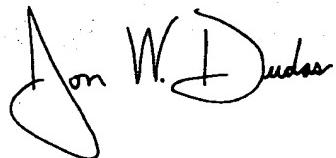
Col. 24, line 13, delete lines 13-23 and insert --an identifying circuit identifying an instruction equivalent to a subroutine return, which corresponds to the instruction equivalent to the subroutine call, based on the result of the comparison

wherein the predicted branch is the branch of the instruction equivalent to the subroutine return in an architecture not including a particular instruction for a subroutine return,

wherein the storing circuit stores the return address of the subroutine as the information specifying the return address, and--.

Signed and Sealed this

First Day of August, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office